

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
30 June 2005 (30.06.2005)

PCT

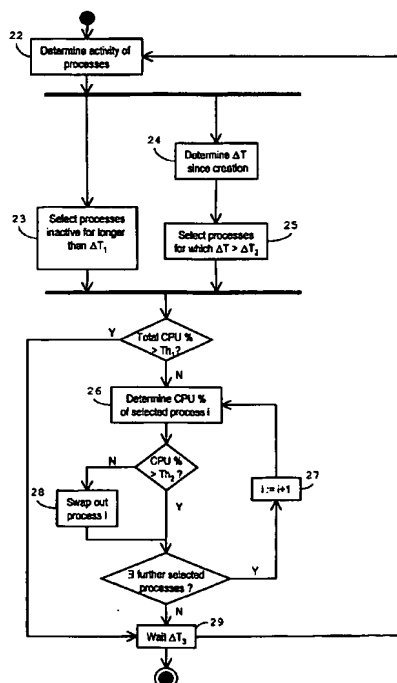
(10) International Publication Number  
**WO 2005/059750 A1**

- (51) International Patent Classification<sup>7</sup>: **G06F 9/50**
- (21) International Application Number:  
PCT/NL2003/000896
- (22) International Filing Date:  
16 December 2003 (16.12.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (71) Applicant (for all designated States except US): **REAL ENTERPRISE SOLUTIONS DEVELOPMENT B.V.** [NL/NL]; Het Zuiderkruis 33, NL-5215 MV 's-Hertogenbosch (NL).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **JANSEN, Peter, Gerardus** [NL/NL]; Fransebaan 563, NL-5627 JX Eindhoven (NL). **JANSSEN, Bob** [NL/NL]; Groenendijk 50, NL-4926 RH Lage Zwaluwe (NL).
- (74) Agent: **DE VRIES, Johannes, Hendrik, F;** De Vries & Metman, Overschiestraat 180, NL-1062 XK Amsterdam (NL).
- (81) Designated States (*national*): AE, AG, AL, AM, AT (utility model), AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ (utility model), CZ, DE (utility model), DE, DK (utility model), DK, DM, DZ, EC, EE (utility model), EE, EG, ES, FI (utility model), FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK (utility model), SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (*regional*): ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE,

[Continued on next page]

(54) Title: MEMORY MANAGEMENT IN A COMPUTER SYSTEM USING DIFFERENT SWAPPING CRITERIA

(57) Abstract: A method of controlling memory allocation in a computer system (1) comprising physical memory (3), at least one storage device (7, 9, 10) and at least one processing unit (2) and arranged to implement virtual memory, which computer system is capable of enabling at least two processes associated with respective instances (14, 17) of application programs to be running, only one active process being enabled to receive input from a user at any one time, comprises selecting at least one of the processes to be at least partially transferred from physical memory (3) to a storage device (7, 9, 10). Processes are selected by monitoring which processes have been inactive for longer than a pre-determined time interval.





SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

**Published:**

— *with international search report*